


Table of Contents		
2	Notes, Block Diagram	
3	PN7221 BGA64 MODULE	

Revisions			
Rev	Description	Date	Approved
X1	Initial Draft	21 March 2022	Jurgen Schroder
A	Release	12 Dec 2022	Jurgen Schroder
A1	Change UI to PN7221EV/C101	14 Mar 2024	Jurgen Schroder

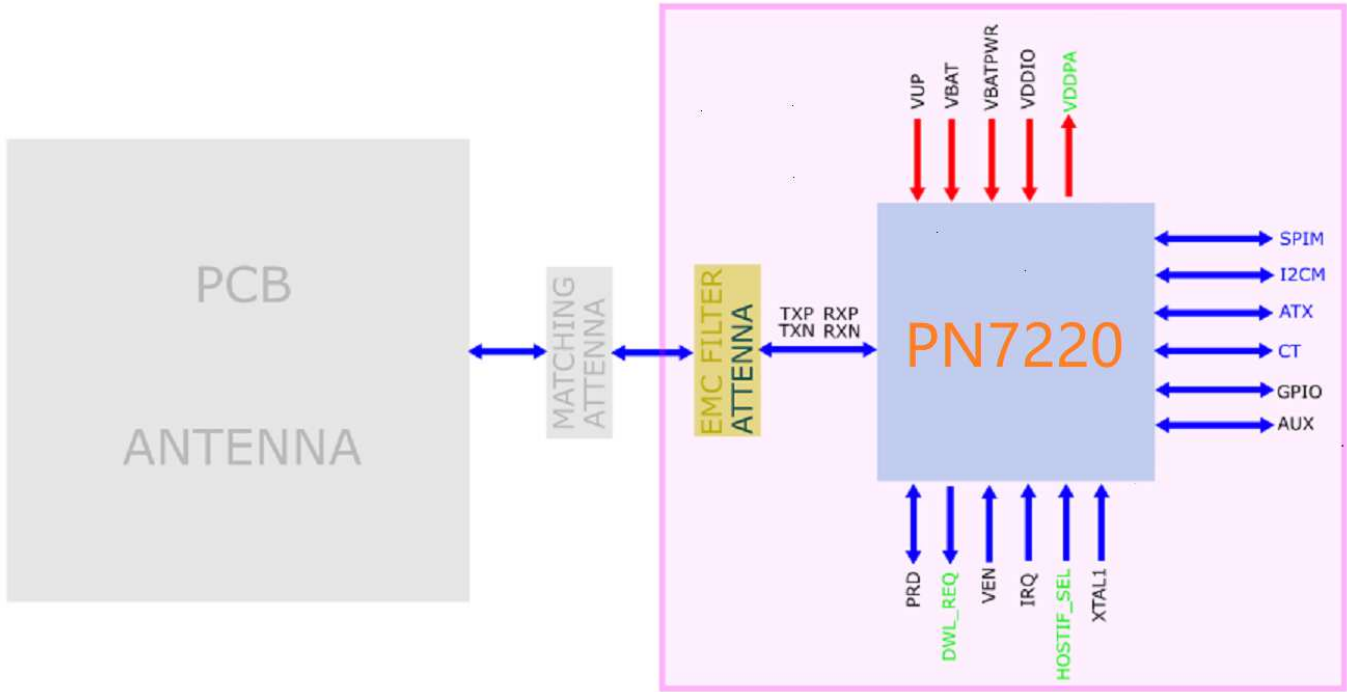
PNEV7220MB

REF DES	ASSY_OPT	PAGE NAME
C10,C27,R1,R5,R7,R8,R9	DNP	03 PN7721 BGA64 MODULE

		Security & Connectivity 6501 William Cannon Drive West Austin, TX 78735-8598	
This document contains information proprietary to NXP and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of NXP Semiconductors.			
© NXP SEMICONDUCTORS		Classification:	<FCP> <FIUO> <PUB>
Designer: Jun Qiao	Drawing Title: PNEV7220MB		
Drawn by: Jun Qiao	Page Title: Title Page		
Approved: Jurgen Schroder	Size C	Document Number SCH-55119 PDF: SPF-55119	Rev A1
Date: Thursday, March 14, 2024		Sheet 1 of 3	

1. Interrupted lines coded with the same letter or letter combinations are electrically connected.
2. Device type number is for reference only. The number varies with the manufacturer.
3. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
4. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

PNEV7220MB: BLOCK DIAGRAM



PN7221 BGA64 MODULE

VBATPWR
GND
VBAT

HOST_SELO
HOST_SEL1
TDA_EN5V_3VN
TDA_CLKDIV2
TDA_CLKDIV1
TDA_EN_1.8V
I2CM_SCL
I2CM_SDA
PRD1
PRD2
CLK_AUX
INT_AUX
IO_AUX
IRQ
DWL_REQ
CLK

ATX A
ATX B
ATX C
ATX D

SWDCLK
SWDIO

VUP
VDDPA
VDDIO
AUX2
AUX1
VEN
VTUNE1
RXP
TX1
TX2
RXN
AUX3

Note: EXT CLK
Remove C1, C2, Y1 and Solder R1 for External NFC CLK option

Layout Note:
XTAL should be as close as possible to IC
XTAL GND island to be connected to VSS_PLL(H5) then drop to board's common GND

NXP

Classification: <FCP> <FIUO> <PUBI>
Drawing Title: PNEV7220MB
Page Title: PN7220 BGA64 MODULE
Date: Thursday, March 14, 2024
Sheet: 3 of 3

